


## APPLICATION DATA SHEET

Electronic Version v14

Stylesheet Version v14.0

<b>Title of Invention</b>	SELF-ALIGNED DRAIN/CHANNEL JUNCTION IN VERTICAL PASS TRANSISTOR DRAM CELL DESIGN FOR DEVICE SCALING		
Application Type : regular, utility Attorney Docket Number : FIS920030209US1			
Correspondence address: Customer Number: 32074 			
<b>Inventors Information:</b>  <u>Inventor 1:</u> <b>Applicant Authority Type:</b> Inventor <b>Citizenship:</b> CN <b>Given Name:</b> Geng <b>Family Name:</b> Wang <b>Residence:</b> <b>City of Residence:</b> Beacon <b>State of Residence:</b> NY <b>Country of Residence:</b> US <b>Address-1 of Mailing Address:</b> 6 South Lockey Woods Road <b>Address-2 of Mailing Address:</b> Apt. A <b>City of Mailing Address:</b> Beacon <b>State of Mailing Address:</b> NY <b>Postal Code of Mailing Address:</b> 12508 <b>Country of Mailing Address:</b> US <b>Phone:</b> <b>Fax:</b> <b>E-mail:</b>  <u>Inventor 2:</u> <b>Applicant Authority Type:</b> Inventor <b>Citizenship:</b> GB <b>Given Name:</b> Kevin <b>Family Name:</b> McStay <b>Residence:</b>			

**City of Residence:** Hopewell Junction  
**State of Residence:** NY  
**Country of Residence:** US  
**Address-1 of Mailing Address:** 71 St. Andrews Lane  
**Address-2 of Mailing Address:**  
**City of Mailing Address:** Hopewell Junction  
**State of Mailing Address:** NY  
**Postal Code of Mailing Address:** 12533  
**Country of Mailing Address:** US  
**Phone:**  
**Fax:**  
**E-mail:**

Inventor 3:

**Applicant Authority Type:** Inventor  
**Citizenship:** US  
**Given Name:** Mary  
**Middle Name:** Elizabeth  
**Family Name:** Weybright  
**Residence:**  
**City of Residence:** Pleasant Valley  
**State of Residence:** NY  
**Country of Residence:** US  
**Address-1 of Mailing Address:** 7 Brenner Ridge Road  
**Address-2 of Mailing Address:**  
**City of Mailing Address:** Pleasant Valley  
**State of Mailing Address:** NY  
**Postal Code of Mailing Address:** 12569  
**Country of Mailing Address:** US  
**Phone:**  
**Fax:**  
**E-mail:**

Inventor 4:

**Applicant Authority Type:** Inventor  
**Citizenship:** CN  
**Given Name:** Yujun  
**Family Name:** Li  
**Residence:**  
**City of Residence:** Poughkeepsie  
**State of Residence:** NY  
**Country of Residence:** US

**Address-1 of Mailing Address:** 510 Maloney Road  
**Address-2 of Mailing Address:** Apt. #K-14  
**City of Mailing Address:** Poughkeepsie  
**State of Mailing Address:** NY  
**Postal Code of Mailing Address:** 12603  
**Country of Mailing Address:** US  
**Phone:**  
**Fax:**  
**E-mail:**

Inventor 5:

**Applicant Authority Type:** Inventor  
**Citizenship:** US  
**Given Name:** Dureseti  
**Family Name:** Chidambarao  
**Residence:**  
**City of Residence:** Weston  
**State of Residence:** CT  
**Country of Residence:** US  
**Address-1 of Mailing Address:** 29 Old Mill Road  
**Address-2 of Mailing Address:**  
**City of Mailing Address:** Weston  
**State of Mailing Address:** CT  
**Postal Code of Mailing Address:** 06883  
**Country of Mailing Address:** US  
**Phone:**  
**Fax:**  
**E-mail:**

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Assignee 1:

**Organization Name:** International Business Machines Corporation  
**Address-1 of Mailing Address:** New Orchard Road  
**Address-2 of Mailing Address:**  
**City of Mailing Address:** Armonk  
**State of Mailing Address:** NY  
**Postal Code of Mailing Address:** 10504  
**Country of Mailing Address:** US

**Phone:**

**Fax:**

**E-mail:**